

Dedicated Pin	100-Pin TQFP	144-Pin TQFP
INPUT/GCLK1	87	125
INPUT/GCLRn	89	127
INPUT/OE1	88	126
INPUT/OE2/GCLK2	90	128
TDI (1)	4	4
TMS (1)	15	20
TCK (1)	62	89
TDO (1)	73	104
GNDINT	38, 86	52, 57, 124, 129
GNDIO	11, 26, 33, 43, 53, 59, 65, 74, 78, 95	3, 13, 17, 26, 33, 59, 64, 77, 85, 94, 105, 114, 135
VCCINT (3.3 V Only)	39, 91	51, 58, 123, 130
VCCIO (2.5 V or 3.3 V)	3, 18, 34, 51, 66, 82	24, 50, 73, 76, 95, 115, 144
No Connect (N.C.)	—	1, 2, 12, 19, 34, 35, 36, 43, 46, 47, 48, 49, 66, 75, 90, 103, 108, 120, 121, 122
Total User I/O Pins (2)	80	96

LAB	MC	100-Pin TQFP	144-Pin TQFP
A	1	2	143
A	2	—	—
A	3	1	142
A	4	—	141
A	5	100	140
A	6	99	139
A	7	—	—
A	8	98	138
A	9	97	137
A	10	—	—
A	11	96	136
A	12	—	134
A	13	94	133
A	14	93	132
A	15	—	—
A	16	92	131
B	17	14	18
B	18	—	—
B	19	13	16
B	20	—	15
B	21	12	14
B	22	10	11
B	23	—	—
B	24	9	10
B	25	8	9
B	26	—	—
B	27	7	8
B	28	—	7
B	29	6	6
B	30	5	5
B	31	—	—
B	32	4 (1)	4 (1)
C	33	25	32
C	34	—	—
C	35	24	31
C	36	—	30
C	37	23	29
C	38	22	28
C	39	—	—
C	40	21	27
C	41	20	—
C	42	—	—
C	43	19	25
C	44	—	23
C	45	17	22
C	46	16	21
C	47	—	—
C	48	15 (1)	20 (1)

LAB	MC	100-Pin TQFP	144-Pin TQFP
D	49	37	56
D	50	—	—
D	51	36	55
D	52	—	54
D	53	35	53
D	54	—	45
D	55	—	—
D	56	32	44
D	57	31	42
D	58	—	—
D	59	30	41
D	60	—	40
D	61	29	39
D	62	28	38
D	63	—	—
D	64	27	37
E	65	40	60
E	66	—	—
E	67	41	61
E	68	—	62
E	69	42	63
E	70	44	65
E	71	—	—
E	72	45	67
E	73	46	68
E	74	—	—
E	75	47	69
E	76	—	70
E	77	48	71
E	78	49	72
E	79	—	—
E	80	50	74
F	81	52	—
F	82	—	—
F	83	—	78
F	84	—	79
F	85	54	80
F	86	55	81
F	87	—	—
F	88	56	82
F	89	57	83
F	90	—	—
F	91	58	84
F	92	—	86
F	93	60	87
F	94	61	88
F	95	—	—
F	96	62 (1)	89 (1)

LAB	MC	100-Pin TQFP	144-Pin TQFP
G	97	63	91
G	98	—	—
G	99	64	92
G	100	—	93
G	101	—	—
G	102	67	96
G	103	—	—
G	104	68	97
G	105	69	98
G	106	—	—
G	107	70	99
G	108	—	100
G	109	71	101
G	110	72	102
G	111	—	—
G	112	73 (1)	104 (1)
H	113	75	106
H	114	—	—
H	115	76	107
H	116	—	109
H	117	77	110
H	118	—	111
H	119	—	—
H	120	79	112
H	121	80	113
H	122	—	—
H	123	81	—
H	124	—	116
H	125	83	117
H	126	84	118
H	127	—	—
H	128	85	119

Notes:

- (1) This pin may function as either a JTAG port or a user I/O pin. When the device is configured to use the JTAG ports for in-system programming, this pin is not available as a user I/O pin.
- (2) The user I/O pin count includes dedicated input pins and all I/O pins.

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