Dedicated Pin	**160-Pin PGA (1)**	**160-Pin PQFP**
INPUT/GCLK1	M8	139
INPUT/GCLRn	N8	141
INPUT/OE1	P8	140
INPUT/OE2/GCLK2	R8	142
TDI (2)	P9	146
TMS (2)	G15	23
TCK (2)	G2	98
TDO (2)	R7	135
GND	C4, C6, C11, D7, D9, D13, G4, H12, J4, M7, M9, M13, N4, N11	3, 18, 32, 47, 57, 64, 66, 81, 96, 111, 126, 138, 143, 148
VCCINT (5.0 V Only)	C7, C9, N7, N9	56, 65, 137, 144
VCCIO (3.3 V or 5.0 V)	C5, C10, C12, D3, G12, H4, J12, M3, N5, N12	10, 25, 40, 55, 74, 89, 103, 118, 133, 155
No Connect (N.C.)	A1, A2, A14, A15, R1, R2, R14, R15	1, 11, 39,54, 67, 82, 110, 120
Total User I/O Pins (3)	124	124

LAB	MC	160-Pin PGA (1)	160-Pin PQFP
A	1	M12	156
A	2	–	–
A	3	P11	154
A	4	–	–
A	5	P12	153
A	6	P10	152
A	7	–	–
A	8	R12	151
A	9	N10	150
A	10	–	–
A	11	R11	149
A	12	–	–
A	13	R10	147
A	14	P9 (2)	146 (2)
A	15	–	–
A	16	R9	145
B	17	L14	8
B	18	–	–
B	19	M14	7
B	20	–	–
B	21	M15	6
B	22	N14	5
B	23	–	–
B	24	N15	4
B	25	P15	2
B	26	–	–
B	27	N13	160
B	28	–	–
B	29	P14	159
B	30	P13	158
B	31	–	–
B	32	R13	157
C	33	H14	21
C	34	–	–
C	35	J13	20
C	36	–	–
C	37	H15	19
C	38	J15	17
C	39	–	–
C	40	J14	16
C	41	K15	15
C	42	–	–
C	43	K13	14
C	44	–	–
C	45	L15	13
C	46	K14	12
C	47	–	–
C	48	L13	9

LAB	MC	160-Pin PGA (1)	160-Pin PQFP
D	49	D15	33
D	50	–	–
D	51	E15	31
D	52	–	–
D	53	E14	30
D	54	F15	29
D	55	–	–
D	56	F13	28
D	57	G14	27
D	58	–	–
D	59	F14	26
D	60	–	–
D	61	G13	24
D	62	G15 (2)	23 (2)
D	63	–	–
D	64	H13	22
E	65	B12	45
E	66	–	–
E	67	B13	44
E	68	–	–
E	69	C13	43
E	70	B14	42
E	71	–	–
E	72	C14	41
E	73	D12	38
E	74	–	–
E	75	B15	37
E	76	–	–
E	77	D14	36
E	78	C15	35
E	79	–	–
E	80	E13	34
F	81	D8	60
F	82	–	–
F	83	A9	59
F	84	–	–
F	85	C8	58
F	86	B9	53
F	87	–	–
F	88	A10	52
F	89	B10	51
F	90	–	–
F	91	A11	50
F	92	–	–
F	93	B11	49
F	94	A12	48
F	95	–	–
F	96	A13	46

LAB	MC	160-Pin PGA (1)	160-Pin PQFP
G	97	A8	61
G	98	–	–
G	99	B8	62
G	100	–	–
G	101	A7	63
G	102	A6	68
G	103	–	–
G	104	B7	69
G	105	A5	70
G	106	–	–
G	107	B6	71
G	108	–	–
G	109	A4	72
G	110	B5	73
G	111	–	–
G	112	D4	75
H	113	A3	76
H	114	–	–
H	115	B4	77
H	116	–	–
H	117	B3	78
H	118	C3	79
H	119	–	–
H	120	B2	80
H	121	B1	83
H	122	–	–
H	123	C2	84
H	124	–	–
H	125	C1	85
H	126	D2	86
H	127	–	–
H	128	D1	87
I	129	E3	88
I	130	–	–
I	131	F3	90
I	132	–	–
I	133	E2	91
I	134	F2	92
I	135	–	–
I	136	E1	93
I	137	G3	94
I	138	–	–
I	139	F1	95
I	140	–	–
I	141	G1	97
I	142	G2 (2)	98 (2)
I	143	–	–
I	144	H1	99

LAB	MC	160-Pin PGA (1)	160-Pin PQFP
J	145	H2	100
J	146	–	–
J	147	J1	101
J	148	–	–
J	149	H3	102
J	150	J3	104
J	151	–	–
J	152	K1	105
J	153	J2	106
J	154	–	–
J	155	K2	107
J	156	–	–
J	157	K3	108
J	158	L1	109
J	159	–	–
J	160	M1	112
K	161	L2	113
K	162	–	–
K	163	N1	114
K	164	–	–
K	165	L3	115
K	166	P1	116
K	167	–	–
K	168	M2	117
K	169	N2	119
K	170	–	–
K	171	P2	121
K	172	–	–
K	173	N3	122
K	174	P3	123
K	175	–	–
K	176	P4	124
L	177	R3	125
L	178	–	–
L	179	R4	127
L	180	–	–
L	181	M4	128
L	182	R5	129
L	183	–	–
L	184	P5	130
L	185	R6	131
L	186	–	–
L	187	P6	132
L	188	–	–
L	189	N6	134
L	190	R7 (2)	135 (2)
L	191	–	–
L	192	P7	136

Notes:
(1) EPM7192S devices are not available in the 160-pin PGA package.
(2) This JTAG pin applies to MAX 7000S devices only and this pin may function as either a JTAG port or a user I/O pin. If the device is configured to use the JTAG ports for ISP, this pin is not available as a user I/O pin.
(3) The user I/O pin count includes dedicated input pins and all I/O pins.